FIG. 1

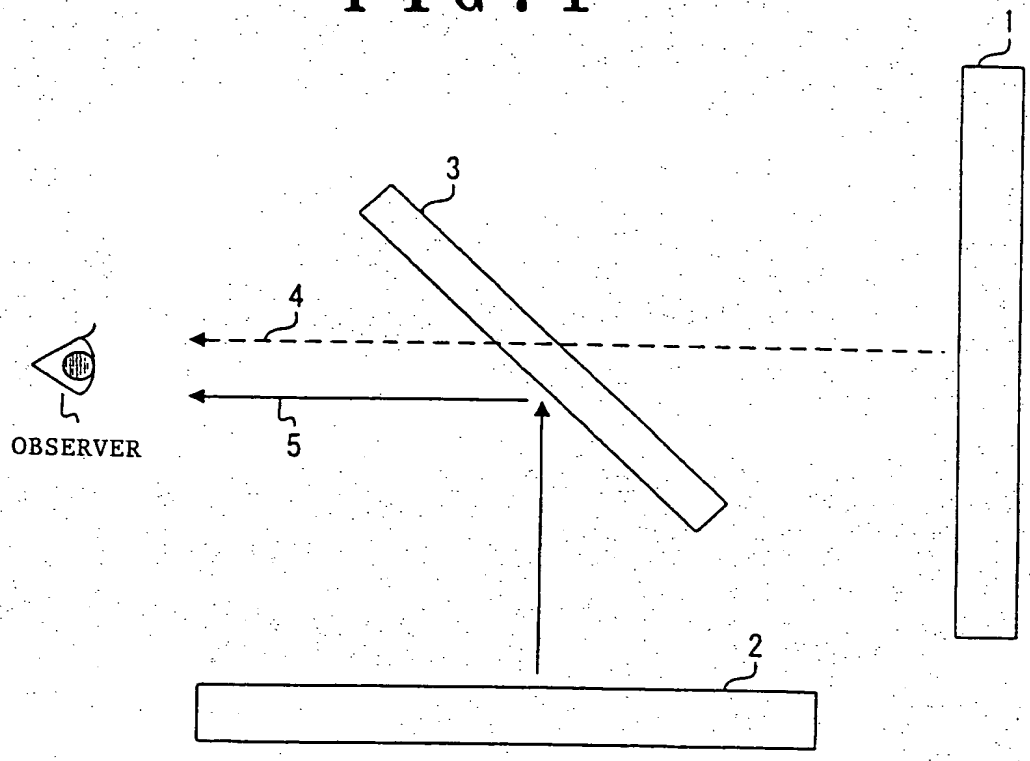


FIG. 2

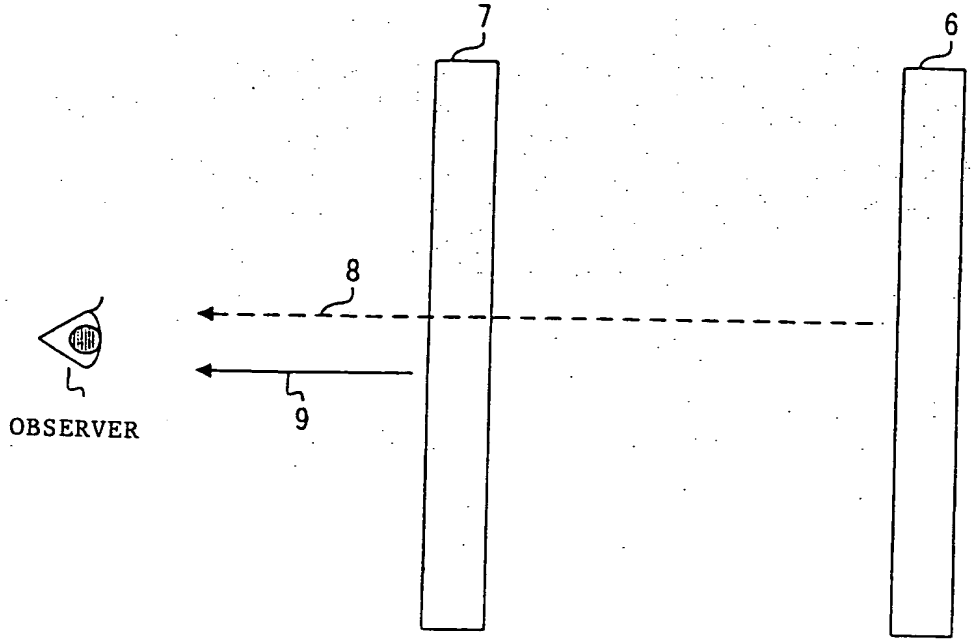


FIG. 3

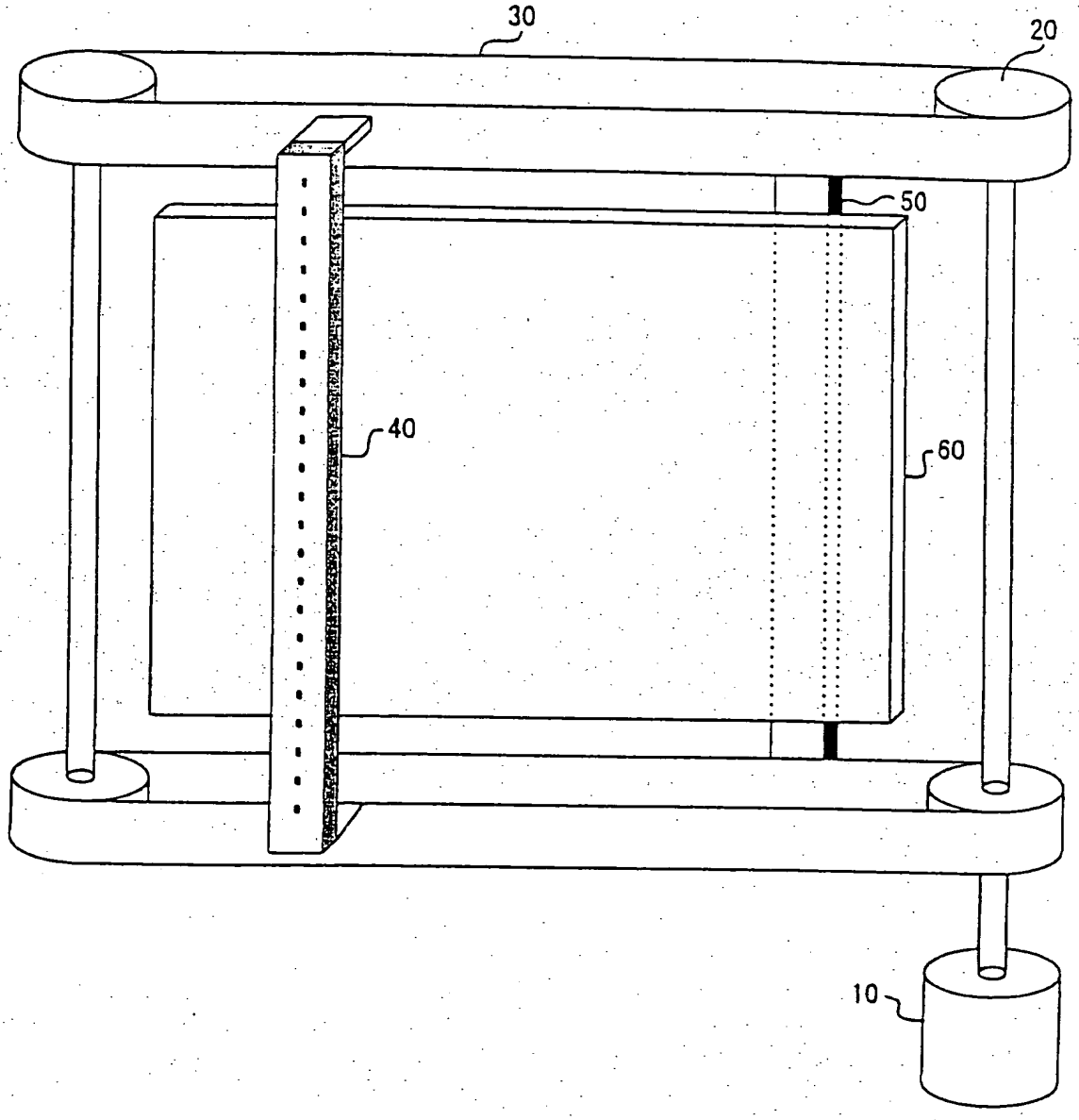


FIG. 4

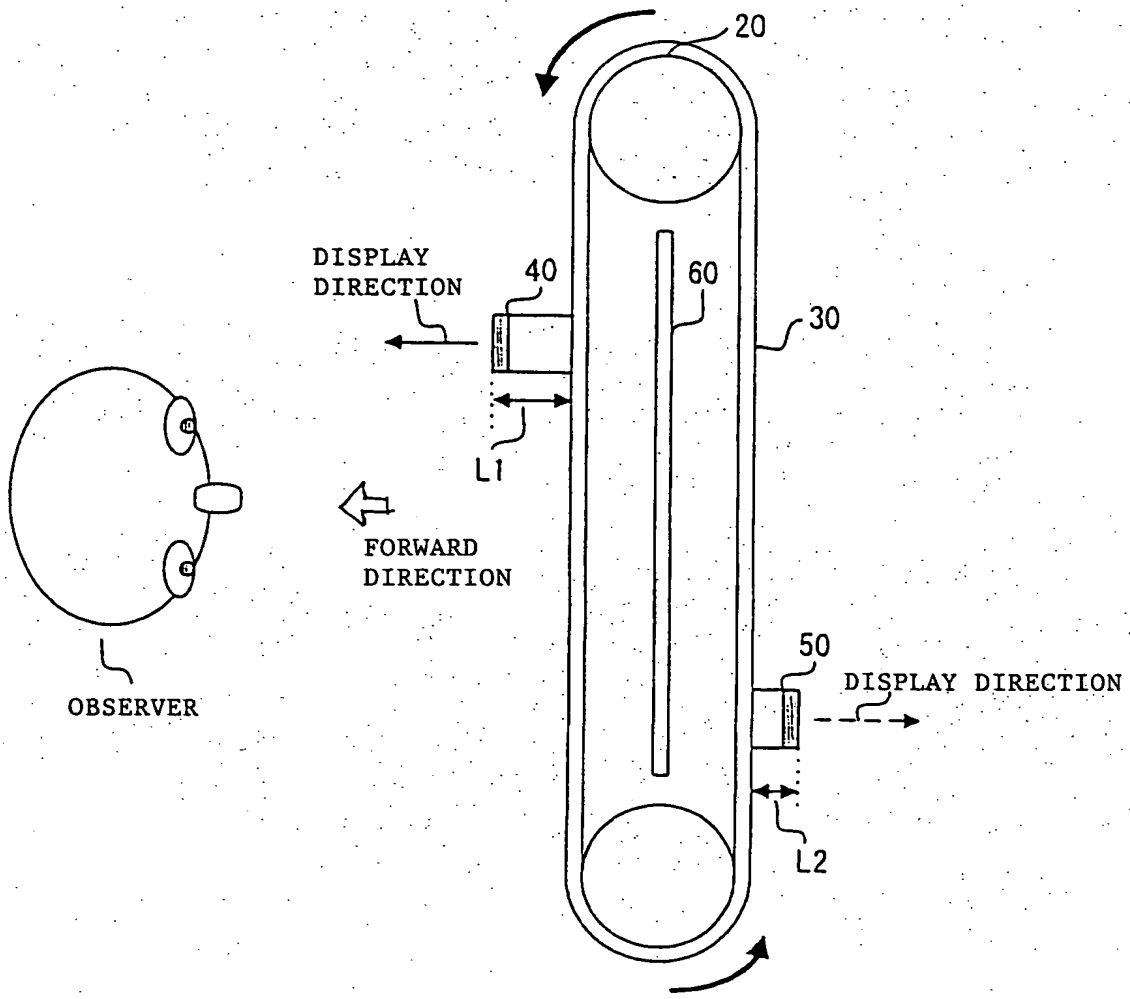

20
DISPLAY
DIRECTION
40
60
30
L1
FORWARD
DIRECTION
OBSERVER
50
DISPLAY DIRECTION
L2

FIG. 5

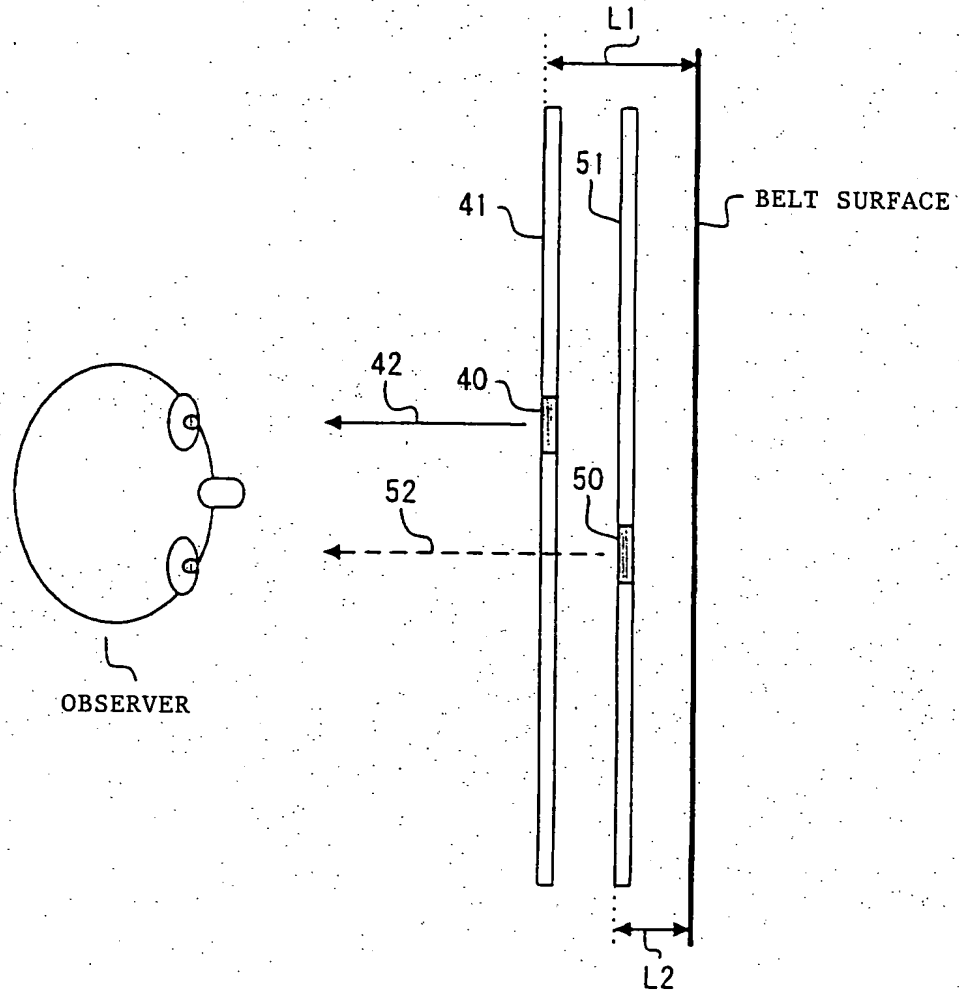


FIG. 6

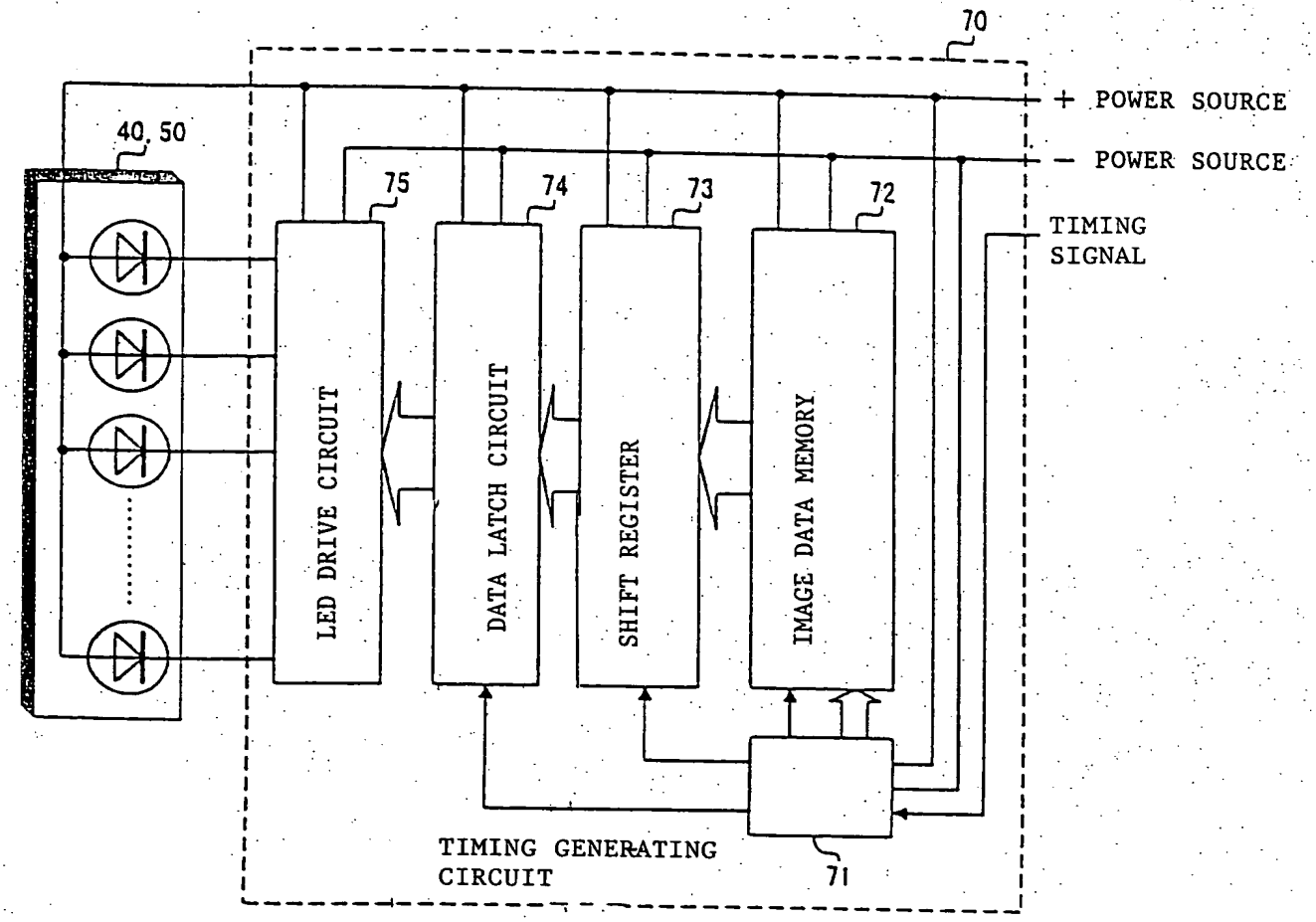

70
+ POWER SOURCE
- POWER SOURCE
TIMING SIGNAL
40, 50
75
74
73
72
LED DRIVE CIRCUIT
DATA LATCH CIRCUIT
SHIFT REGISTER
IMAGE DATA MEMORY
TIMING GENERATING CIRCUIT
71

FIG. 7

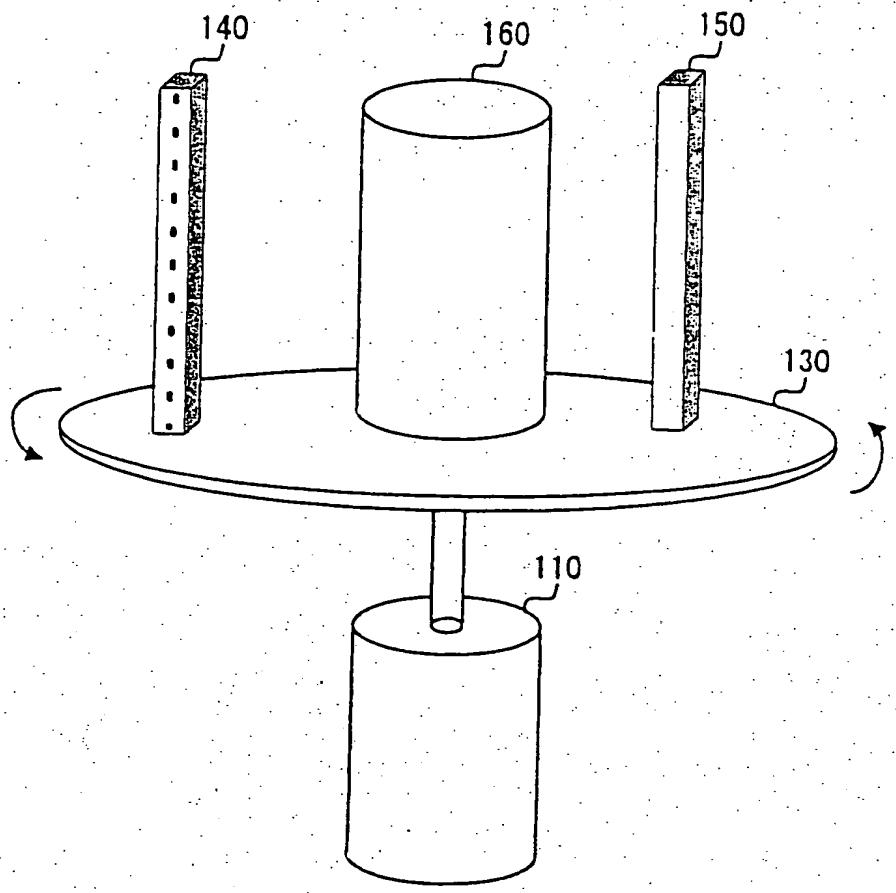


FIG. 8

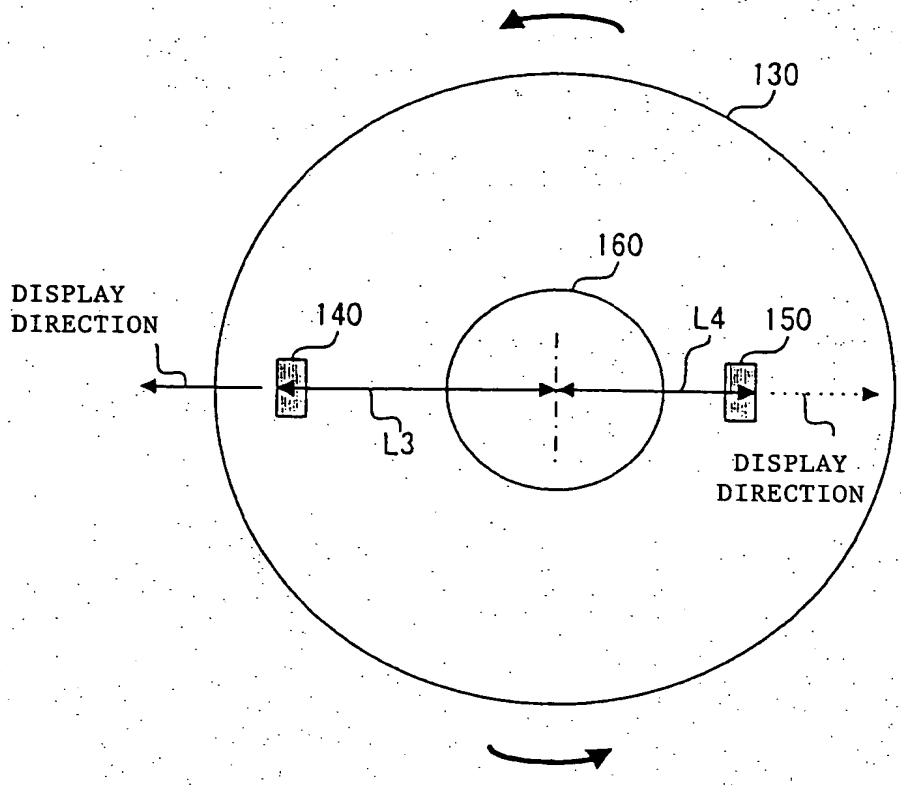
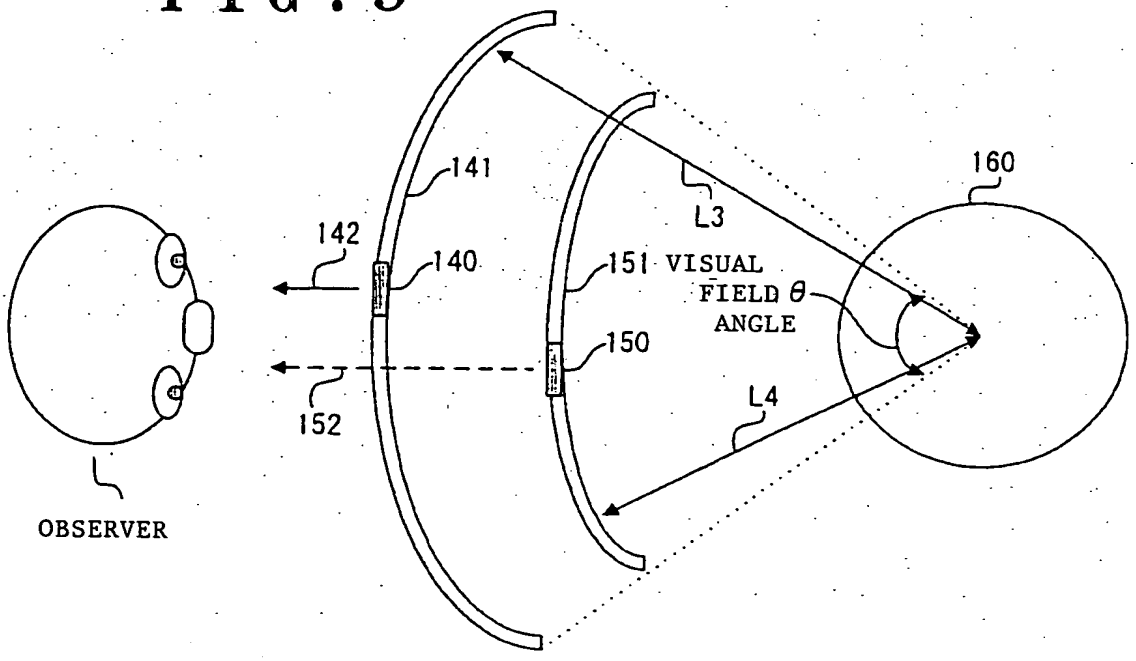

130
160
DISPLAY DIRECTION
140
L4
150
L3
DISPLAY DIRECTION

FIG. 9


141
142
140
151
VISUAL FIELD θ ANGLE
150
152
L3
160
L4
OBSERVER